

### **REMARKS/ARGUMENTS**

In response to the Examiner's FINAL Office Action of November 17, 2008 issued in relation to the present Patent Application, the Applicant submits the below Remarks.

Claims 1-6, 8-18 and 20 are pending in the application.

#### ***Regarding Response to Arguments***

Examiner in the Response to argument has confirmed that Mi teaches applying the XOR function to identifier 211 and a secret key to obtain a return value.

Firstly, claim 1 requires for the identifier to be mapped into a key K, not applying a key to the identifier. Thus, the claim requires for the input to be the identifier, and the output to be the key. Mi teaches applying the XOR function to the identifier 211 and the key to obtain a return value. Thus Mi teaches having the identifier and key as inputs, and the return value as the output. Thus, Applicant maintains that Mi fails to teach in each integrated circuit, mapping the identifier into a key K.

Secondly, the secret key is never mapped from the processor number. In other words, the secret key is never the output of a mapping, with the processor number being the input. The secret key is part of each verification agent 212. (Paragraph [0024]) Thus, the secret key is a constant that pre-exists.

The Examiner appears to misconstrue the feature of mapping  $p$  into  $q$ , which mathematically may be  $F(p)=q$ , with  $F$  being a mapping function. Mi teaches  $p \text{ XOR } q = r$ .

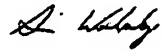
It is therefore maintained that claim 1, and all claims dependent on claim 1, are allowable over the prior art of record.

### **CONCLUSION**

It is respectfully submitted that all of the Examiner's rejections have been traversed. Accordingly, it is submitted that the present application is in condition for allowance and reconsideration of the present application is respectfully requested.

Very respectfully,

Applicant/s:



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